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Application Number	10/626,366
Filing Date	July-23, 2003
First Named Inventor	Hong Lim
Group Art Unit	2811
Examiner Name	Nitin Parekh
Attorney Docket Number	LSI1P225 / 03-0466

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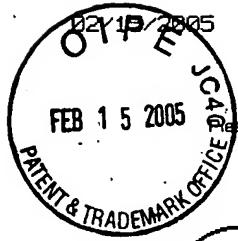
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Name	SANDEEP JAGGI, Chief IP Counsel and Assistant Corporate Secretary, LSI Logic Corp.	
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Application Number	10/626,366
Filing Date	July-23, 2003
First Named Inventor	Hong Lim
Group Art Unit	2811
Examiner Name	Nitin Parekh
Attorney Docket Number	LSI1P225 / 03-0466

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Hong Lim et. al.

Serial No. : 10/626,366Group Art Un 2811Filed : July 23, 2003Examiner: Parekh, NitinFor : Integrated Circuit Package and Method for a PBGA Package Having a Multiplicity of Staggered Power Ring Segments For Power Connection to Integrated Circuit DieAtty Docket : LSI1P225 / 03-0466

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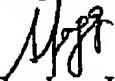
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The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

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Respectfully submitted,


Sandeep Jaggi
Chief IP Counsel
Corporate Assistant Secretary
LSI LOGIC CORPORATION

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
(408) 433-7475

Date: 2/14/05